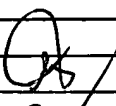
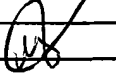


FORM PTO-1449/A and B (Modified)		APPLICATION NO.: --	ATTY. DOCKET NO.: H0682.70002 US00
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: November 10, 2003	CONFIRMATION NO.: --
		APPLICANT: Patrick J. Garavan	
Sheet 1 of 1	GROUP ART UNIT: --		EXAMINER: --

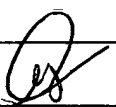
#### U.S. PATENT DOCUMENTS

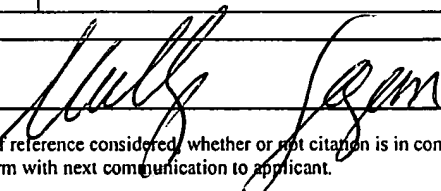
Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
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#### FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document*			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			

#### OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	
		TUTHILL, Mike, "IEEE Journal of Solid State Circuits, Vol. 33, No. 7, July 1998, pp. 1117-1122.		

EXAMINER 	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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